

TITLE: "A MEMORY DEVICE WITH AN ALTERNATING VSS INTERCONNECTION"

INVENTOR(S): Timothy Thurgate and Richard Fastow USSN: 10/823,972 Attorney Docket #: AMD-H0546

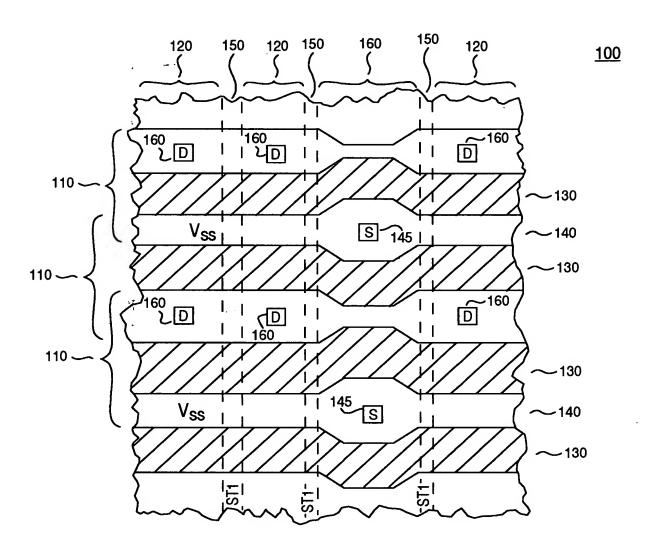
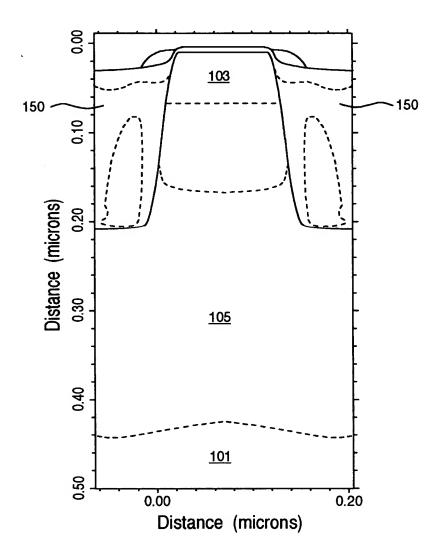


Fig. 1A PRIOR ART

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(Prior Art)

Fig. 1B

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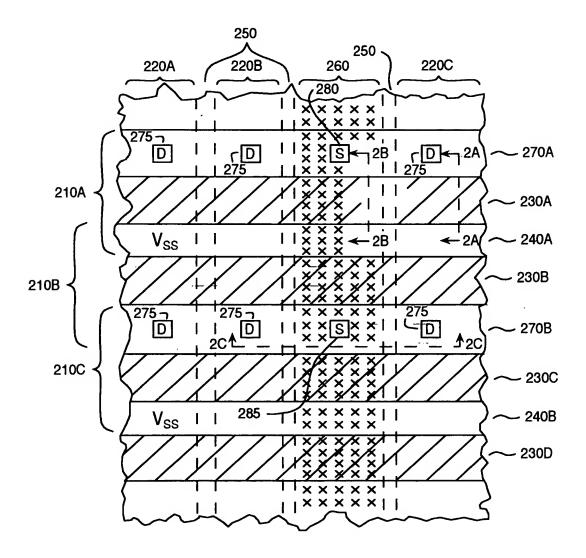


Fig. 2

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<u>300</u>

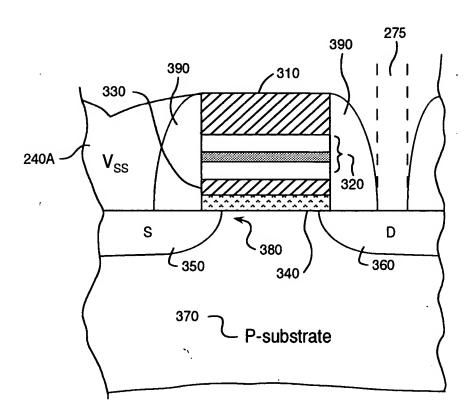


Fig. 3

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<u>400</u>

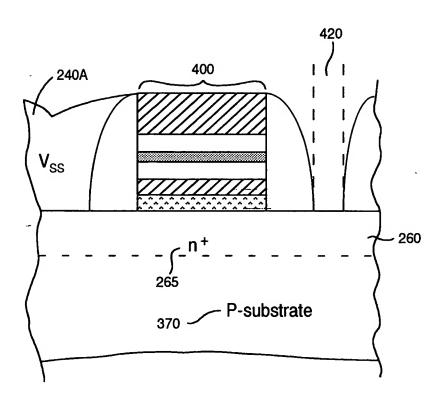


Fig. 4

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<u>500</u>

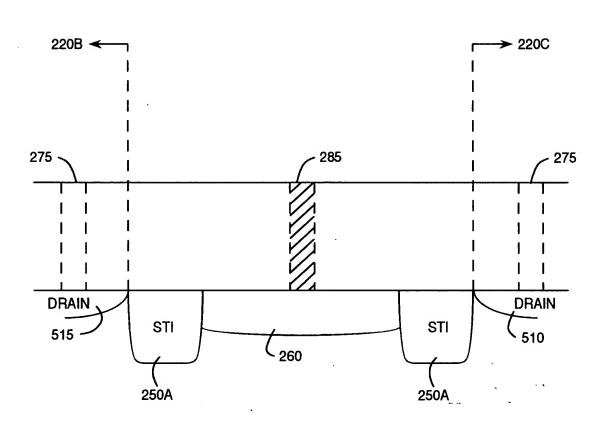


Fig. 5

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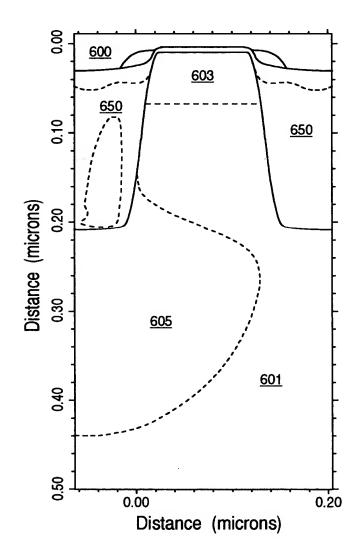
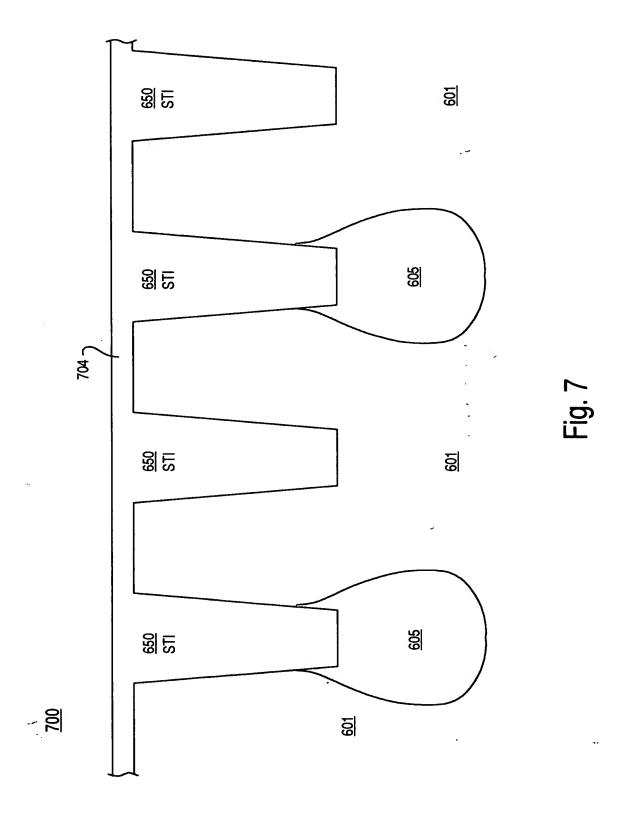


Fig. 6



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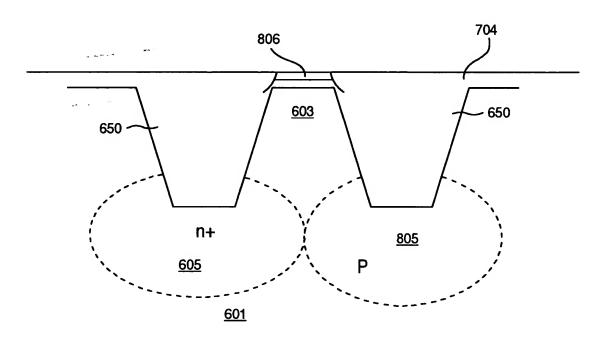


Fig. 8

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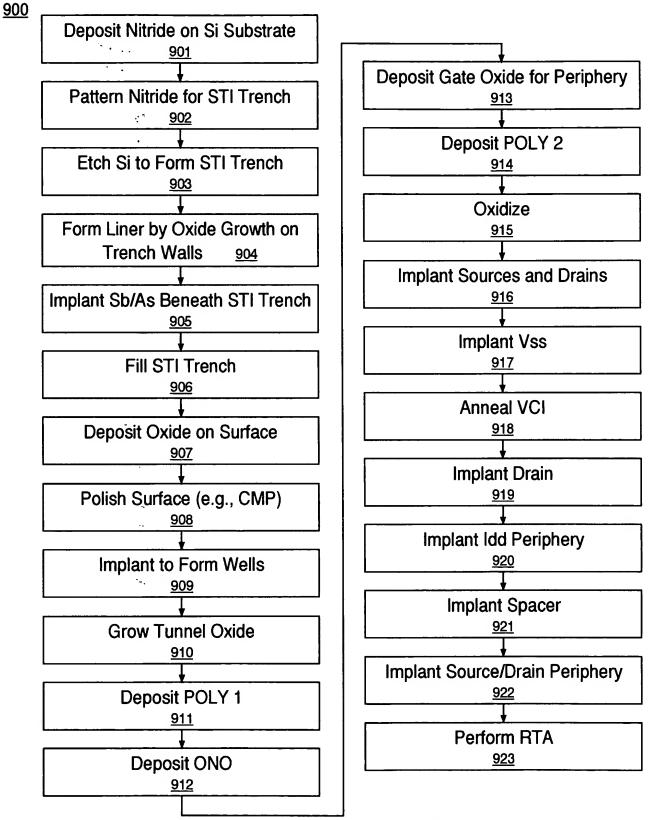


Fig. 9